Oxidation-Resistant Amorphous TaN Barrier for MIM-Ta₂O₅ Capacitors in Giga-Bit DRAMs

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We demonstrate that an amorphous TaN with no grain boundaries reveals a good oxidation-resistant performance after annealing the Ta $_2O_5$ dielectric (550°C, O_2). We fabricated an MIM-TaO capacitor with a concave-type Ru SN on the TaN barrier metal. This showed a contact resistivity of 0.27 k Ω •µm², a capacitance of 20 fF/bit, and a leakage current of 0.9 fA/bit (–1 to 1 V). We further fabricated a crown-type Ru SN to demonstrate scalability to 0.10-µm design rule.