75 word Abstract

Title:

A study of analog characteristics of CMOS with heavily nitrided NO oxynitrides

Authors:

T. Ohguro, T. Nagano, M. Fujiwara, M. Takayanagi, T. Shimizu, H.S.Momose, S. Nakamura and Y. Toyoshima

Address:

System LSI Research & Development Center, Semiconductor Company, Toshiba Corp

8, Shinsugita-cho, Isogo-ku, Yokohama, Kanagawa 235-8522, Japan Tel:+81-45-770-3644, Fax:+81-45-770-3575, email:tatsuya.ooguro@toshiba.co.jp

Abstract:

We study any analog characteristics of CMOS with heavily nitrided NO oxynitrides. A 1/f noise and ft in surface channel PMOS degrade due to higher donor-like trap density and lower hole mobility, respectively. Thus, the interface state density at oxynitride/Si substrate should be cared in order to satisfy 1999 ITRS road map requirements for each technology generation. And these degradations of PMOS can be suppressed and analog characteristics of NMOS can be improved by changing from a surface to a buried channel type MOSFET.