ABSTRACT

A Novel Analysis Method of Threshold Voltage Shift due to Detrap in a Multi-level Flash Memory

Ren-ichi Yamada, Tomoko Sekiguchi, Yutaka Okuyama, Jiro Yugami, and Hitoshi Kume Central Research Laboratory, Hitachi, Ltd.,

1-280 Higashi-Koigakubo, Kokubunji, Tokyo, 185-8601, Japan

Phone: +81-42-323-1111; Fax: +81-42-327-7774; e-mail: renichi@crl.hitachi.co.jp

With the aim of improving flash-memory retention characteristics, we investigated threshold voltage shift $(\Delta V_{\rm th})$ due to charge detrap from tunnel oxide. Accordingly we propose new parameter that can reveal the main origin of detrap (hole/electron) and the detrap centroid. We found that the main origin of detrap changes from holes to electrons depending on the degree of tunnel-oxide degradation. Since the hole detrap increases $V_{\rm th}$ of a programmed memory cell, this $V_{\rm th}$ increase must be considered, especially when designing a multi-level flash memory.