

# **A 40-GHz Frequency Divider in 0.18- $\mu\text{m}$ CMOS Technology**

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## **Abstract**

**A frequency divider employs resonance techniques by means of on-chip spiral inductors to operate at high speeds. Configured as two cascaded  $\div 2$  stages, the circuit achieves a frequency range of 2.3 GHz at 40 GHz while consuming 31 mW from a 2.5-V supply.**