2006 VLSI Circuits Short Course

This year the Circuits Short Course is comprised of two programs, Analog and Digital. The cost of the short course includes both programs and attendees will receive one book for both programs. Attendees will be able to move back and forth between the two programs.

Analog Short Course Program **Data Converter Design for Embedded Systems**	Digital Short Course Program **Designing for Paradigm Shifts in Microprocessors and Networking**

Honolulu I
Wednesday, June 14, 8:10 a.m.

Organizers/Chairs: Katsu Nakamura, Analog Devices
                     Hiroshi Yamazaki, Fujitsu Labs

8:10 a.m. — **Introduction**
K. Nakamura, Analog Devices

8:15 a.m. — **Introduction to Embedded Data Converters**
A. Matsuzawa, Tokyo Institute of Technology

9:25 a.m. — **High-Speed Analog-to-Digital Converters**
M. Pelgrom, Philips Research

10:35 a.m. — **Break**

10:50 a.m. — **Precision Analog-to-Digital Converters**
C. Lyden, Analog Devices

12:00 p.m. — **Lunch**

1:30 p.m. — **High-Speed and Precision Digital-to-Analog Converters**
M. Hotta, Musashi Institute of Technology

2:40 p.m. — **Break**

2:55 p.m. — **Practical Considerations for Embedded Data Converters**
J. Wieser, National Semiconductor

4:05 p.m. — **Design for Testability of Data Converter Circuits in Embedded Systems**
G. Roberts, McGill University

5:15 p.m. — **Conclusion**
H. Yamazaki, Fujitsu Labs

Honolulu II
Wednesday, June 14, 8:10 a.m.

Organizers/Chairs: Steven Butler, AMD
                     Hideyuki Kabuo, Panasonic

8:10 a.m. — **Introduction**
S. Butler, Advanced Micro Devices

8:15 a.m. — **Technology**
M. Hane, NEC

9:25 a.m. — **Architecture**
C. Moore, Advanced Micro Devices

10:35 a.m. — **Break**

10:50 a.m. — **SOC Integration**
P, Rickert, Texas Instruments

12:00 p.m. — **Lunch**

1:30 p.m. — **Security**
A. Satoh, IBM Japan

2:40 p.m. — **Break**

2:55 p.m. — **Sensor Networking**
K. Suzuki, Hitachi

4:05 p.m. — **Power-Constrained Performance**
S. Rusu, Intel

5:15 p.m. — **Conclusion**
H. Kabuo, Panasonic