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│        SESSION I – TAPA I        │
│         Plenary Session          │
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Tuesday, June 13, 8:00 a.m. – 10:00 a.m.
Chairpersons: Jason Woo, University of California
                Tohru Mogami, NEC Corp.

8:15 a.m.   Welcome and Opening Remarks
                Bob Havemann, Novellus Systems, Inc.
                Shin'ichiro Kimura, Hitachi, Ltd.

1.1 – 8:30 a.m.
Balancing SoC Design and Technology Challenges at 45nm, Hans Stork, Texas Instruments

1.2 – 9:15 a.m.
Interactive Humanoids and Androids, Hiroshi Ishiguro, Osaka University