

JOINT TECHNOLOGY/CIRCUITS RUMP
SESSION

Tuesday, June 17
8:00 p.m – 10:00 p.m.

Organizers:

Circuits

G. Lehmann, Infineon

K. Arimoto, Renesas

Technology

T. Grider, Texas Instruments

Y. Omura, Kansai University

RJ1: Ten years after – Has SOI finally arrived?

Tapa I

Moderators: L. Counts, LC Consulting
K. Ishimaru, Toshiba America

Ten years after introduction of SOI into a production as a competitor to bulk-CMOS, both technologies continue to co-exist in some of the application domains, such as microprocessors and gaming. On the other hand, mainstream SoC's exclusively use bulk-CMOS. A panel similar to this one 10 years ago has concluded that SOI has a performance advantage over bulk, and the cost will be its main barrier for wide adoption. Has anything changed and will it change in the next decade? Will SOI penetrate more market segments or disappear? Does further technology scaling require the migration to fully-depleted SOI?

Panelists:

M. Bohr, Intel

G. Shahidi, IBM

E. Suzuki, AIST

A. Kameyama, Toshiba

R. Mahnkopf, Infineon

C. Mazure, SOITEC

D. Scott, TSMC

M. Usami, Hitachi