

2011 Symposia on VLSI Technology and Circuits

as of June 8, 2011

Please note the following changes to the program

*Changes are underlined

TECHNOLOGY				
Date & Time	Room	Program Information	Old (in Digest)	New
June 14 (Tue.) 10:40-12:20	Shunju II	SESSION 2B: RRAM I	Chairpersons: Y. Akasaka (TOKYO ELECTRON TAIWAN LIMITED) <u>J. Lutze (Sandisk Corp.)</u>	Chairpersons: Y. Akasaka (TOKYO ELECTRON TAIWAN LIMITED) <u>J. Zahurak (Micron Technology, Inc.)</u>
June 14 (Tue.) 13:40-15:45	Shunju II	SESSION 3B: RRAM II	Chairpersons: S. S. Chung (National Chiao Tung Univ.) <u>J. Zahurak (Micron Technology, Inc.)</u>	Chairpersons: S. S. Chung (National Chiao Tung Univ.) <u>M. Khare (IBM Corp.)</u>
June 16 (Thu.) 14:20-16:00	Shunju II	SESSION 11B: MRAM and NAND	Chairpersons: N. Kasai (Tohoku Univ.) <u>J. Lutze (Sandisk Corp.)</u>	Chairpersons: N. Kasai (Tohoku Univ.) <u>T. Skotnicki (STMicroelectronics)</u>

CIRCUITS				
Date & Time	Room	Program Information	Old (in Digest)	New
June 14 (Tue.) 9:15-10:15	Suzaku III	Workshop Program: "Bio Inspired Computation -What Electronics can learn from Bio-"	<u>Super-sonic Imaging Using MEMS Sensor Array</u> Shuntaro Machida (Hitachi, Ltd.)	<u>Ultrasound Imaging using MEMS Sensor Array</u> Shuntaro Machida (Hitachi, Ltd.)
June 15 (Wed.) 10:30-12:35	Suzaku I	SESSION 2: Switching DC-DC Converters	Chairpersons: C. Yoo (Hanyang Univ.) <u>U.-K. Moon (Oregon State Univ.)</u>	Chairpersons: C. Yoo (Hanyang Univ.) <u>J. Savoj (Xilinx)</u>
June 15 (Wed.) 13:55-16:00	Suzaku I	SESSION 5: Circuit and System Integration	Chairpersons: C. Hou (TSMC) <u>J. Barth (IBM Microelectronics)</u>	Chairpersons: C. Hou (TSMC) <u>B. Nikolic (Univ. of California, Berkeley)</u>
June 16 (Thu.) 10:30-12:35	Suzaku I	SESSION 11: Fractional-N PLLs	Chairpersons: S.H. Cho (KAIST) <u>N. Kurd (Intel Corp.)</u>	Chairpersons: S.H. Cho (KAIST) <u>U.-K. Moon (Oregon State Univ.)</u>
June 16 (Thu.) 14:20-16:00	Suzaku I	SESSION 13: High Speed Digital for Interconnects	Chairpersons: M. Igarashi (Sony Corp.) <u>C. Sechen (Univ. of Texas at Dallas)</u>	Chairpersons: M. Igarashi (Sony Corp.) <u>J. Wieser (National Semiconductor Corp.)</u>
June 16 (Thu.) 16:15-17:55	Suzaku I	SESSION 15: Clocking Building Blocks	Chairpersons: H.-J. Park (Pohang Univ. of Science and Technology (POSTECH)) <u>J. Tierno (IBM TJ Watson Research Center)</u>	Chairpersons: H.-J. Park (Pohang Univ. of Science and Technology (POSTECH)) <u>A. Emami (CalTech)</u>
June 16 (Thu.) 20:00-22:00	Suzaku I, II	RUMP SESSION: R-1: Special Evening Session : NVM Technology and New Application Opportunities	Speaker: <u>E. Doller (Micron Technology, Inc.)</u>	Speaker: <u>K. Winters (Micron Technology, Inc.)</u>
June 17 (Fri.) 16:15-17:55	Suzaku I	SESSION 27: Signal Processing for Wireline	Chairpersons: <u>R. Kuppuswamy (Intel India)</u> J. Gealow (MediaTek Wireless, Inc.)	Chairpersons: <u>K. Sunaga (NEC Corp.)</u> J. Gealow (MediaTek Wireless, Inc.)

The following paper has been canceled

TECHNOLOGY		
Date & Time	Room	Program Information
June 16 (Thu.) 10:55-11:20	Shunju I	10A-2 (Invited): From 3D-SOC to 3D Heterogeneous Systems: Technology and Applications P. Ancey (STMicroelectronics)