Dear Editor; Apr. 10, 2013

Attached are the press release materials for 2013 Symposia on VLSI Technology and Circuits, which will be held on Jun. 10-14, 2013 at Rihga Royal Hotel Kyoto, Kyoto, Japan.

About 1,000 of the world's leading experts in the field of microelectronics will gather here for the 2013 Symposia on VLSI Technology and Circuits, The Symposia alternate between here and Hawaii annually and serve as the premier mid-year gatherings to present research for the advancement of microelectronics technology and circuit development.

Please see the attached files for the details.

Further Information, please visit <u>www.vlsisymposium.org</u> or contact to Secretariats.

Thank you very much for your attention about this matter,

Hitoshi Wakabayashi Makoto Nagata Symposium Chair, Symposium Chair,

2013 Symposium on VLSI Technology 2013 Symposium on VLSI Circuits

Toshiro Hiramoto Hideyuki Kabuo

Technical Program Chair, Technical Program Chair,

2013 Symposium on VLSI Technology 2013 Symposium on VLSI Circuits

Satoshi Inaba Makoto Takamiya

Publicity Chair (JFE), Publicity Chair (JFE),

2013 Symposium on VLSI Technology 2013 Symposium on VLSI Circuits

Mukesh Khare Gunther Lehmann

Publicity Chair (NAE), Publicity Chair (NAE),

2013 Symposium on VLSI Technology 2013 Symposium on VLSI Circuits