

## 2015 SYMPOSIA ON VLSI TECHNOLOGY & CIRCUITS - CONFERENCE SCHEDULE

Technology  Circuits

	Time	Suzaku III	Suzaku II	Suzaku I	Shunju III	Shunju II	Shunju I
Sunday, June 14	7:30-17:00				Technology Registration		
Monday, June 15	8:30-17:00				Technology Short Course		
Tuesday, June 16	7:30-17:00	Technology/Circuits Registration					
	8:20-10:05				T1: Welcome and Plenary Session		
	10:30-12:10				T2: Highlight		
	10:30-17:10	Circuits Short Course 2		Circuits Short Course 1			
	13:30-15:35				T3: (FS) 7nm Node Logic Technology and Beyond		T4: Reliability
	15:50-17:30				T5: (FS) 3D Systems and Packaging		T6: Advanced CMOS Technology: Ge FinFET / Compact Model
	19:30-20:00				35th Anniversary Reception		
	20:00-22:00		Technology Evening Panel Discussion		Joint Evening Panel Discussion		
Wednesday, June 17	7:30-17:00	Technology/Circuits Registration					
	8:30-10:05				C1: Welcome and Plenary Session		
	10:30-12:35	C3: SARADCs & SC Filter	C2: Image Processing		JFS1: Ultra Low Power for IoT		T7: Memory Technology: PC RAM
	13:55-16:00	C5: Low Power Wireless Transceivers	C4: Image Sensors		JFS2: Emerging NVM		T8: Process Technology: Contact / Interconnect
	16:15-17:55	C7: Optical Links	C6: Bio Monitoring Circuits		T9: Memory Technology: ReRAM		T10: Advanced CMOS Technology: Nanowire FET / 2D Materials
	19:00-21:00	Joint Banquet					
	Thursday, June 18	8:00-17:00	Technology/Circuits Registration				
8:30-10:10		C9: Phase and Delay Locked Loops	C8: (FS) Systems for Big Data Management		T11: Advanced CMOS Technology: Si FinFET Device & Process		T12: Memory Technology: MTJ and Related Devices
10:30-12:35		C11: Nyquist ADC and DAC	C10: (FS) IoT and Smart Systems		T13: Advanced CMOS Technology: X-On Insulator(X-OI) Devices		T14: Memory Technology: 3D NAND Flash & Other NVM
12:45-14:05					Luncheon Talk		
14:20-16:00		C12: DRAM	JFS3: Advanced Technology and Circuits for IoT		T15: Non-Si Substrates: III-V HEMT/FET/TFET		T16: Beyond CMOS and New Concepts
16:15-17:55		C14: Application-Specific IOs	JFS4: 3D and Heterogeneous Integration	C13: Sensors & Bio Imaging			
20:00-22:00		Circuits Evening Panel Discussion 2		Circuits Evening Panel Discussion 1			
Friday, June 19	8:00-15:00	Circuits Registration					
	8:30-10:10	C17: Low-Power and Secure Design	C16: Oscillators	C15: Ultra-High Speed Receivers			
	10:30-12:35	C20: Power Management Circuits	C19: SRAM and CAM	C18: Wideband Over-Sampled ADCs			
	13:55-16:00	C23: Advanced Technologies for Processors	C22: High Speed and High Frequency TX/RX	C21: Delta-Sigma Modulators and Analog Techniques			
	16:15-17:55	C26: Low-Power Wireline Transceivers	C25: DC-DC Converters	C24: Displays and Sensors			