2016 SYMPOSIUM ON VLSI CIRCUITS

Hilton Hawaiian Village, HONOLULU, Hawaii
Tuesday – Friday, June 14-17, 2016
(June 14 Short Course,
June 15-17 Technical Sessions)

The 2016 Symposium on VLSI Circuits welcomes the submission of original papers on all aspects of VLSI Circuits. The VLSI Technology Symposium (reverse side) will be held at the same location with two days of overlap. A single registration allows participants to attend both Symposia, and offers excellent opportunities to synergize on topics of joint interest.

Symposium Scope

Papers will be selected based on technical innovation, advances relative to previously published work, credibility of claims, and quality of writing and illustrations in the following areas:

- **Digital circuits and techniques** for standalone and embedded processors
- **Memory circuits, architectures and interfaces** for volatile and non-volatile memories, including emerging technologies
- **Clock generation and distribution** for high-frequency digital and mixed-signal applications
- **Analog and mixed-signal circuits**, including amplifiers, filters, and data converters
- **Wireline receivers and transmitters**, including circuits for inter-chip and long-reach applications
- **Wireless receivers and transmitters**, and circuits for WAN, LAN, PAN, BAN inter-chip. and mm-wave applications
- **Power management circuits**, including those for battery management, voltage regulation, and energy harvesting
- **Application-oriented circuits and VLSI systems**, imagers, displays, sensors and biomedical and healthcare applications

Best Student Paper Award

Best Student Paper Award selection will be based on quality of the written paper and the Symposium presentation. The winning student will be presented a monetary award and a certificate at the opening session of the 2017 Symposium. The winner must be enrolled as a full-time student at the time of submission and be the leading author and the presenter of the paper.

Information and Registration

The Symposia's website provides additional information, including information regarding registration and hotel reservations. To obtain assistance with any aspect of the Symposium, please contact the appropriate Secretariat.

Secretariat for VLSI Symposia (America and Europe)
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Joint Circuits and Technology Focus Sessions

Joint circuits and technology focus sessions will be offered in the following special topics of joint interest:

- **Design in scaled technologies:** scaling of digital, memory, analog and mixed-signal circuits in advanced CMOS processes
- **Design enablement:** design for manufacturing, process-design co-optimization, on-die monitoring of variability and reliability
- **Embedded memory technology and design:** SRAM, DRAM, Flash, PCRAM, RRAM, MRAM, etc.
- **3D and heterogeneous integration:** power and thermal management, inter-chip communication, SiP architectures and applications

Innovative System Directions

Authors are encouraged to submit papers on "big integration" that showcase innovations that extend beyond a single IC and into the module. These include focus areas of **Internet of Things, Industrial Electronics, Big Data Management, Biomedical Applications** and **Robotics and Smart Cars**. New focus sessions comprising invited and contributed papers will be offered.

Paper Submission

Prospective authors must submit two-page papers and abstracts using the Symposia's website, www.vlsisymposium.org. Accepted papers will be published as submitted with **no revisions permitted**. Authors must follow detailed instructions provided within the "Authors" section of the website, including the Authors' Guide and Pre-publication Policy. The technical content beyond the abstract of the accepted paper must not be announced, published, or in any way put in the public domain prior to the Symposium. Partial travel expense support for students who are presenting papers is available upon request. Extended versions of selected papers from the Symposium will be published in a **Special Issue of the IEEE Journal of Solid-State Circuits**.

For complete conference, author, and registration information, visit:
http://www.vlsisymposium.org/

Paper Submission Deadline: 23:59 PST Monday, January 25, 2016

2016 VLSI CIRCUITS SYMPOSIUM HONOLULU

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IEEE   SSCS   Electron Devices Society   EiC

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