©Toji Temple

2019 SYMPOSIUM ON VLSI CIRCUITS

Pushing the Limits of Semiconductors for United and Connected World

RIHGA Royal Hotel Kyoto, Japan
Sunday – Friday, June 9-14, 2019
June 9 Sunday Workshop
June 10 Short Course
June 11-13 Technical Sessions
June 14 Friday Forum

Paper Submission Deadline: 23:59 JST Monday, January 28, 2019
For detail, please visit: www.vlsisymposium.org

Symposium Scope

The Circuits Symposium is placing special emphasis on several Innovative System focus areas, and encourages paper submissions on:

• Machine/deep learning
• FPGA-based accelerators
• Internet of things
• Industrial electronics
• Big data management
• Biomedical applications
• Robotics and autonomous transportation

Focus sessions comprising invited and contributed papers will be offered.

In addition, submissions are welcomed in all of the following circuits areas:
• Processors, SoCs, and machine learning
• Digital circuits, signal integrity, and IOs
• Hardware security
• Memory circuits, architectures, and interfaces
• Biomedical circuits
• Sensors, imagers, and display circuits
• Power conversion circuits
• Analog, amplifier and filter circuits
• Wireless receivers and transmitters
• Data converters
• Frequency generation and clock circuits
• Wireline receivers and transmitters

Call for Workshop

The call for proposals for the Sunday Workshop is now open. You organize the workshop, VLSI provides the venue. Check the details in
www.vlsisymposium.org/satellite-workshops-events/

Short Courses: A full day Short Courses will offer in-depth lectures on some of the topics of highest interest to the VLSI community given by distinguished experts
• New computing: From security to AI and everything in between (Joint with Technology)
• 5G Systems (Circuits)

Joint Circuits and Technology Focus Sessions

Joint circuits and technology focus sessions will be offered in the following special topics of joint interest:
• Technology & system for AI
• The future of memory
• Design and technology for scaling extension
• 3D and heterogeneous integration

Friday Forum
will give broad perspectives on how semiconductors are shaping the future of transportation and autonomous driving.

Demo Session for Outstanding Papers:
A demo session will be held during the Joint Circuits/Technology Reception on Monday. At the demo session the authors of the selected papers will showcase some of the most exciting recent innovations in the field.

Paper Submission

Prospective authors must submit two-page camera-ready papers and abstracts using the Symposia's website, www.vlsisymposium.org. Accepted papers will be published as submitted, with **no revisions permitted**. Authors must follow detailed instructions provided within the "Authors" section of the website, including the Authors' Guide and Pre-publication Policy. Extended versions of selected papers from the Symposium will be published in a **Special Issue of the IEEE Journal of Solid-State Circuits**.

Best Student Paper Award

Best Student Paper Award selection will be based on quality and presentation of the paper at the Symposium. The winning student will be presented a monetary award and a certificate at the 2020 Symposium opening session. **The student must be enrolled as a full-time student at the time of submission, be the leading author and presenter of the paper**, and indicate when submitting the paper that the paper should be considered for the award.

Secretariat for VLSI Symposia
(Asia and Japan) c/o JTB Communication Design, Inc., Tokyo, Japan
+81-3-5657-0777, vlsisymp@jtbcom.co.jp
(America and Europe) IEEE Meetings, Conferences & Events (MCE), Piscataway, NJ USA +1 732 465 6620, vlsisymposium@ieee.org

Symposium Chair:
Makoto Ikeda
University of Tokyo
ikeda@silicon.u-tokyo.ac.jp

Symposium Co-Chair:
Ken Chang
Xilinx Inc.
ken.chang@xilinx.com

Program Chair:
Ken Takeuchi
Chuo University
takeuchi@takeuchi-lab.org

Program Co-Chair:
Brian Ginsburg
Texas Instruments
bginzz@ti.com

Join the Symposia on VLSI Technology & Circuits LinkedIn Group